

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

EDWARD T. GROCHOWSKI, ET AL.

Application No.: 09/973,429

Filed: October 9, 2001

For: **PREDICATE PREDICTION IN A
PROCESSOR (As Amended)**

Art Group: 2183

Examiner: Aimee J. Li

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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Alexandria, VA 22313-1450

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The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: 3/21/05

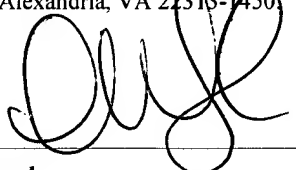
Respectfully submitted,

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	09/973,429
Filing Date	10-09-2001
First Named Inventor:	Grochowski, et al.
Art Unit	2183
Examiner Name	Aimee J. Li
Attorney Docket Number	042390.P4899C

Sheet

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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		DULONG, ET AL., "An Overview of the Intel® IA-64 Compiler", Intel Technology Journal Q4, 1999, Pages 1-15.	
		Intel® Itanium™ Architecture Software Developer's Manual, Part II: Optimization Guide for the Intel® Itanium™ Architecture, Predication, Control Flow, and Instruction Stream, Volume 1: Application Architecture, Revision 2.0, December 2001, Pages 1:129, 1:155 – 1:163.	
		Intel® Itanium™ Processor, Product Highlights, Copyright © 2001, Intel Corporation.	
		TOM R. HALFILL, "Beyond Pentium II", BYTE.com, December 1997, Pages 1-10.	
		JIM TURLEY, "64-Bit CPUs: What you Need to Know", Extreme Tech, February 8, 2002, Pages 1-12.	
		NICHOLAS P. CARTER, ECE 412-Advanced Computer Architecture, Lecture 4: Predication, September 13, 2000, Pages 1-17.	
		PNEVMATIKATOS ET AL., "Guarded Execution and Branch Prediction in Dynamic ILP Processors", Proceedings of the 21 st Annual International Symposium on Computer Architecture, IEEE, pp. 120-129.	
		MAHLKE ET AL., "Characterizing the Impact of Predicated Execution Branch Prediction", Proceedings of the 27 th Annual International Symposium Microarchitecture, Micro-27, IEEE, Nov. 30-Dec. 2, 1994, pp. 217-227.	

Examiner Signature		Date Considered	
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